[METHOD OF SAVING/RESTORING PROCESSOR STATE AFTER ENTERING/EXITING DEBUG MODE]

Abstract of Disclosure

A method of saving/restoring a processor state after entering/exiting a debug mode. When a processor desires to perform a debugging process, the processor enters a debug mode. A resume instruction is produced. The program counter of the resume instruction is stored into a register. The debugging instructions are performed without need to count the number of the instructions having been executed during the debug mode. When the processor wants to exit the debug mode, then the processor fetches the program counter stored in the register, and jumps back to the resume instruction.

Figures